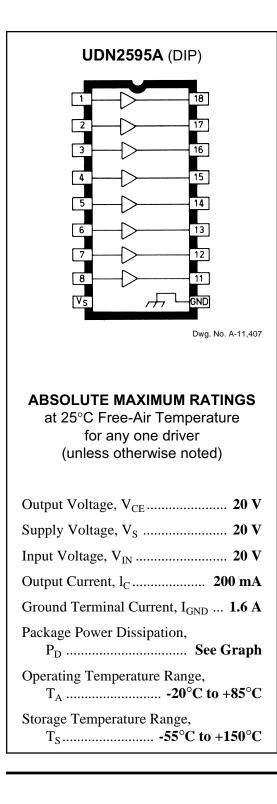
2595

8-CHANNEL SATURATED SINK DRIVER



Developed for use with low-voltage LED and incandescent displays requiring low output saturation voltage, the UDN2595A and A2595SLW meet many interface needs, including those exceeding the capabilities of standard logic buffers. The eight non-Darlington outputs of each driver can continuously and simultaneously sink load currents of 100 mA at ambient temperatures of up to +75°C.

The eight-channel driver's active-low inputs can be driven directly from TTL, Schottky TTL, DTL, 5 to 16 V CMOS, and NMOS logic. All input connections are on one side of the package, output connections on the other, for simplified printed wiring board layouts.

These drivers are packaged in plastic DlPs (suffix A) or surfacemountable wide-body SOICs (suffix LW), and are rated for operation over the temperature range of -20° C to $+85^{\circ}$ C. The A2595SLW is also available for operation to -40° C. To order, change the suffix from 'SLW' to 'ELW'.

FEATURES

- Non-Inverting Function (Input Low = Output ON)
- 200 mA Current Rating
- 100 mA Continuous and Simultaneous (All outputs) to +85°C
- Low Saturation Voltage
- TTL, CMOS, NMOS Compatible
- Efficient Input/Output Pin Format
- DIP or SOIC Packaging

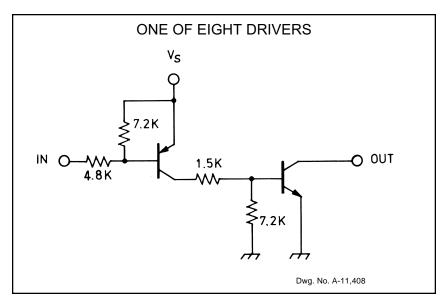
Always order by complete part number:

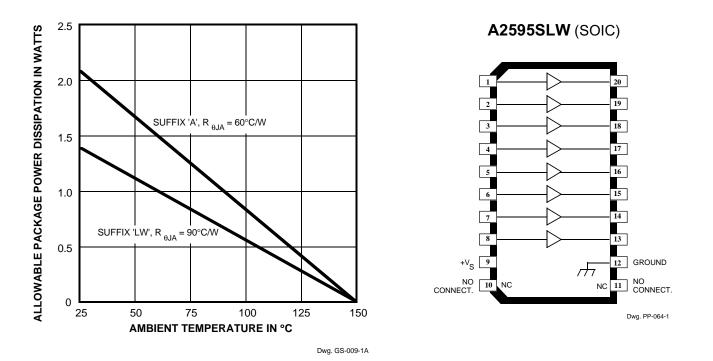
Part Number	Package		
UDN2595A	18-Pin DIP		
A2595SLW	20-Lead Wide-Body SOIC		



This datasheet has been downloaded from http://www.digchip.com at this page

FUNCTIONAL BLOCK DIAGRAM





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ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_S = 5.0$ V (unless otherwise noted).

			Limits		
Characteristic	Symbol	Test Conditions	Min.	Max.	Units
Output Leakage	I _{CEX}	$V_{IN} \ge 4.5 \text{ V}, V_{OUT} = 20 \text{ V}, T_A = 25^{\circ}\text{C}$	_	50	μA
Current		$V_{IN} \ge 4.6 \text{ V}, V_{OUT} = 20 \text{ V}, T_A = 70^{\circ}\text{C}$	_	100	μA
Output Saturation	V _{CE(SAT)}	V _{IN} = 0.4 V, I _{OUT} = 50 mA	—	0.5	V
Voltage		V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	0.6	V
Input Current	I _{IN(ON)}	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	-1.6	mA
		V _{IN} = 0.4 V, I _{OUT} = 100 mA, V _S = 15 V	_	-5.0	mA
Input Voltage	V _{IN(ON)}	I_{OUT} = 100 mA, $V_{OUT} \le 0.6 V$	_	0.4	V
	V _{IN(OFF)}	I _{OUT} = 100 μA, T _A = 70°C	4.6	_	V
Input Capacitance	C _{IN}		_	25	pF
Supply Current	ا _S	V _{IN} = 0.4 V, I _{OUT} = 100 mA	_	6.0	mA
		V _{IN} = 0.4 V, I _{OUT} = 100 mA, V _S = 15 V	_	20	mA

NOTES: 1. Negative current is defined as coming out of the specified device pin.

2. The V_{IN(ON)} voltage limit guarantees a minimum output sink current per the specified conditions.

3. l_s is measured with any one of eight drivers turned ON.

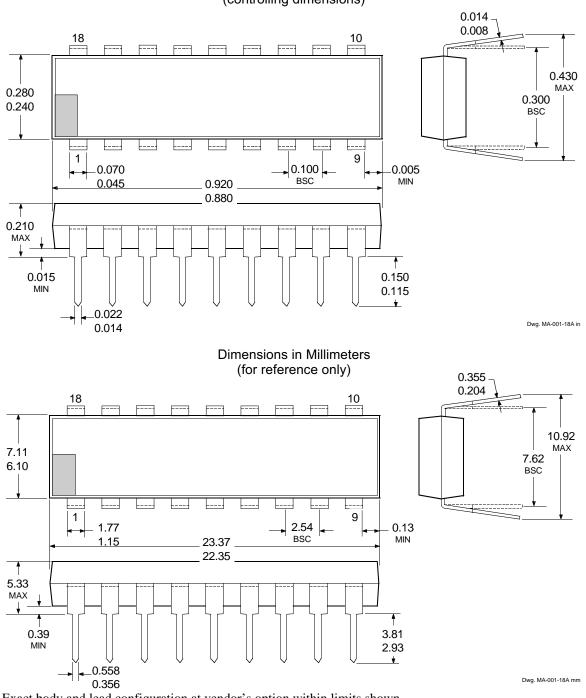
The products described here are manufactured under one or more U.S. patents or U.S. patents pending.

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The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.

UDN2595A



Dimensions in Inches (controlling dimensions)

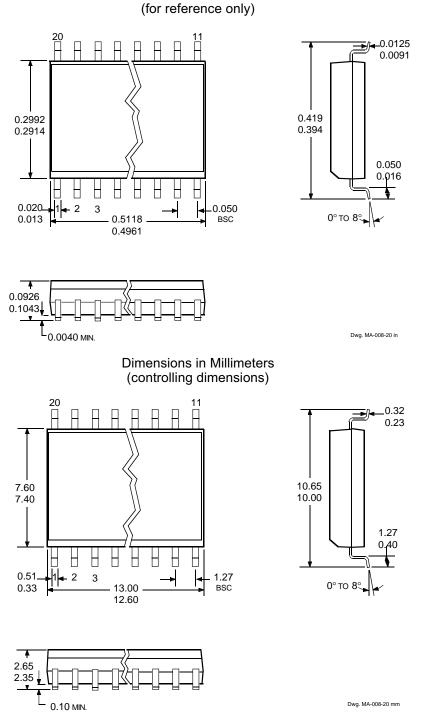
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
 - 2. Lead spacing tolerance is non-cumulative.
 - 3. Lead thickness is measured at seating plane or below.



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A2595SLW

Dimensions in Inches



NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

2. Lead spacing tolerance is non-cumulative.

POWER SINK DRIVERS

IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

Output Ratings *		Features						
		Serial Latched Diode				Internal		
mA	V	#	Input	Drivers	Clamp	Outputs	Protection	Part Number [†]
75	17	8	X	Х		constant current	-	6275
	17	16	Х	Х	_	constant current	_	6276
100	20	8	_	_	_	saturated	_	2595
	30	32	Х	Х	_	_	_	5833
	40	32	Х	Х	_	saturated	-	5832
	50	8	addre	essable decod	der/driver	DMOS	_	6B259
	50	8	_	Х	_	DMOS	_	6B273
	50	8	Х	Х	-	DMOS	-	6B595
120	24	8	Х	Х	-	constant current	_	6277
250	50	8	addre	addressable decoder/driver			-	6259
	50	8	_	Х	-	DMOS	-	6273
	50	8	Х	X	-	DMOS	_	6595
	50	8	_	_	Х	saturated	_	2596
	60	4	_	_	Х	saturated	Х	2557
350	50	4	_	Х	Х	_	_	5800
	50	7	_	_	Х	_	_	2003
	50	7	_	_	Х	_	_	2004
	50	8	_	_	Х	_	_	2803
	50	8	_	Х	X	_	_	5801
	50	8	Х	X X	_	_	-	5821
	50	8	Х		Х	-	-	5841
	50	8	addre	addressable decoder/driver DMOS –				6A259
	50	8	Х	Х	-	DMOS	-	6A595
	80	8	X X	Х	_	-	-	5822
	80	8	Х	Х	Х	-	-	5842
	95	7	-	-	Х	-	-	2023
	95	7	-	-	Х	-	-	2024
450	30	28	dual 4- to 14-line decoder/driver – –					6817
600	60	4	_	-	-	saturated	Х	2547
	60	4	-	-	Х	saturated	Х	2549 and 2559
700	60	4	—	-	Х	saturated	Х	2543
750	50	8	_	_	Х	saturated	_	2597
1000	46	4	stepp	per motor con	troller/drive		_	7024 and 7029
1200	46	4		stepping con			_	7042
1250	50	4	stepp	er motor tran	slator/drive	er –	Х	5804
1800	50	4	_	_	Х	_	_	2540
3000	46	4		per motor con			_	7026
	46	4		stepping con			-	7044
4000	50	4			Х	_	_	2878

* Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

† Complete part number includes additional characters to indicate operating temperature range and package style.

